

Notice of References Cited	Application/Control No. 10/750,175	Applicant(s)/Patent Under Reexamination DANG ET AL.	
	Examiner John P. Trimmings	Art Unit 2138	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,550,843	08-1996	Yee, Wilson K.	714/726
*	B	US-5,651,013	07-1997	Iadanza, Joseph Andrew	714/731
*	C	US-5,675,589	10-1997	Yee, Wilson K.	714/30
*	D	US-6,331,790	12-2001	Or-Bach et al.	326/41
*	E	US-6,842,039	01-2005	Guzman et al.	326/38
*	F	US-6,968,487	11-2005	Bryant et al.	714/726
*	G	US-6,983,405	01-2006	Herron et al.	714/726
*	H	US-6,996,758	02-2006	Herron et al.	714/726
*	I	US-2005/0138509	06-2005	Kiryu et al.	714/726
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Renovell et al., "IS-FPGA: A New Symmetric FPGA Architecture with Implicit SCAN", November 1, 2001, IEEE International Test Conference 2001, pp 924-931.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.